

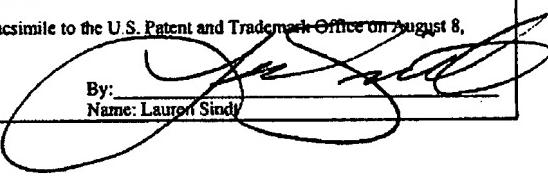
AUG 08 2007

S/N 10/521908

PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	TSUKADA	Examiner:	HOANG, TU BA
Serial No.:	10/521908	Group Art Unit:	2832
Filed:	January 20, 2005	Docket No.:	10921.0269USWO
Title:	CHIP RESISTOR AND MANUFACTURING METHOD THEREFOR		

**CERTIFICATE UNDER 37 CFR 1.6(d):**  
I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on August 8, 2007.

By:   
Name: Lauren Sindt

Mail Stop: AMENDMENT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

AMENDMENT

Dear Commissioner:

In response to the Office Action mailed February 8, 2007, please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims that begins on page 2 of this paper.

**Remarks/Arguments** begin on page 4 of this paper.